

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
11	BRS	L64	32714	clock same (selector multiplexor mux multiplexing)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/08/03 10:14	
14	BRS	L67	6999	64 and clock same (selector multiplexor mux multiplexing) same (combinatorial combining combination logic)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/08/03 10:16	
15	BRS	L68	2822	67 and clock same (selector multiplexor mux multiplexing) same (combinatorial combining combination logic) with output	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/08/03 13:22	
16	BRS	L69	1694	68 and (selector multiplexor mux multiplexing) with (two second)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/08/03 10:17	
17	BRS	L70	63	69 and clock with (DLL delay\$2 adj lock\$2 adj loop)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/08/03 12:29	